

**In the Specification**

Please add the following new paragraph after the paragraph beginning on page 6, line 15:

Figure 4A is a modification to the circuit of Figure 2 including a series device block provided at the input to the comparator circuitry.

Please replace the paragraph beginning on page 8, line 13 with the following amended paragraph:

In a modification to the arrangement of Figure 4, shown in Figure 4A~~Although not shown~~, it will be understood that series devices 407 can be added, such as one or more additional MOS devices provided between the first MOS device 200 and the comparator, to block the current signal path. These series devices are shown schematically as a series device block 407. A resultant effect of such a clamp may be to clamp the output at a first level. It will be appreciated that the configuration of these MOS devices, if provided, may be suitably altered depending on the application. As is known to those skilled in the art, it will be possible to add further devices to clamp the output, as desired.